



**SRI RAAJA RAAJAN**  
**COLLEGE OF ENGINEERING AND TECHNOLOGY**

(Approved by AICTE, New Delhi & Affiliated to Anna University, Chennai)

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**DEPARTMENT OF**  
**ELECTRONICS AND COMMUNICATION ENGINEERING**

**LABORATORY PHOTOS**

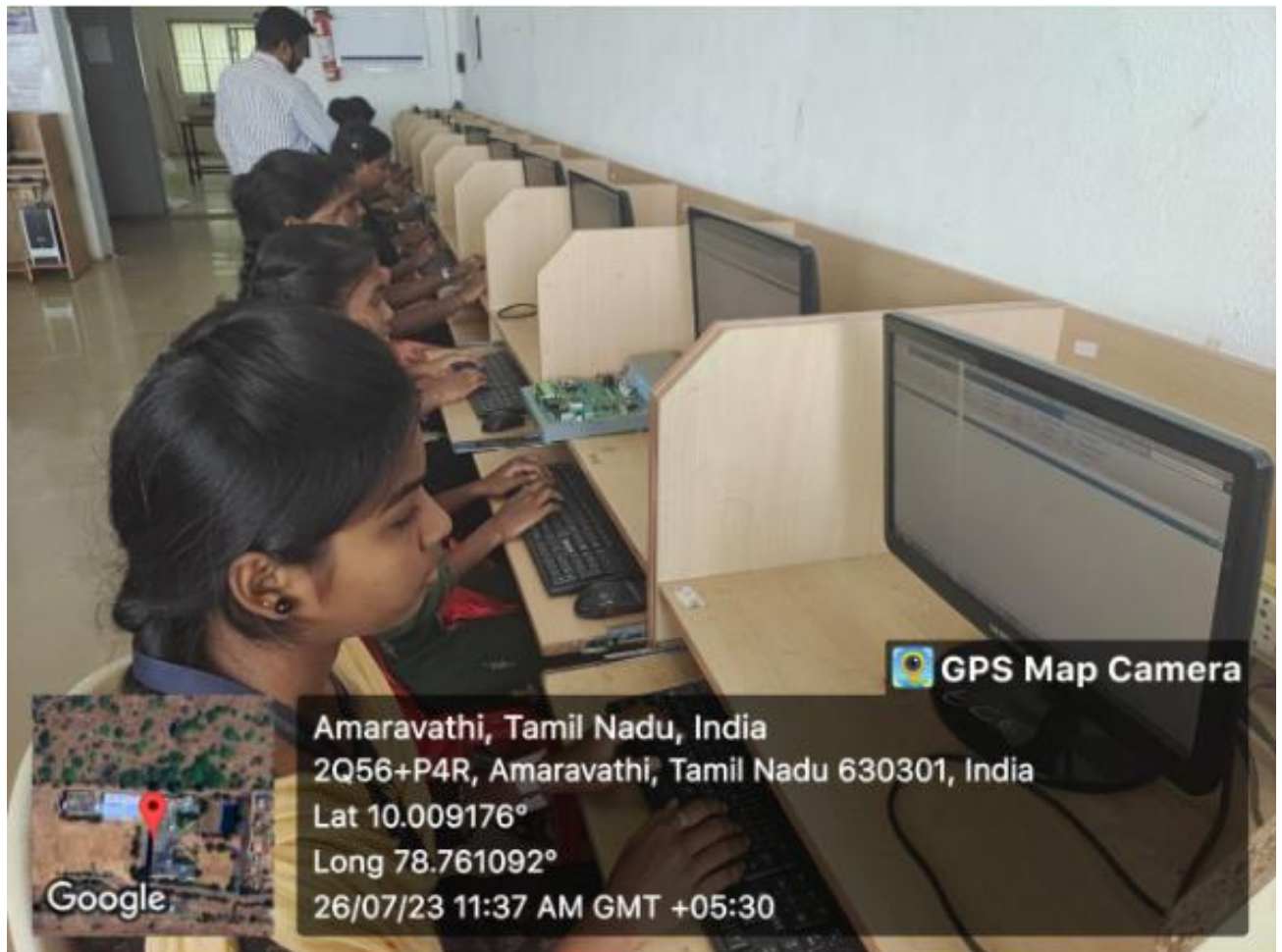
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## DIGITAL SIGNAL PROCESSING LABORATORY



### **(i) Frequency Analysis using DFT**




  
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## DIGITAL SIGNAL PROCESSING LABORATORY



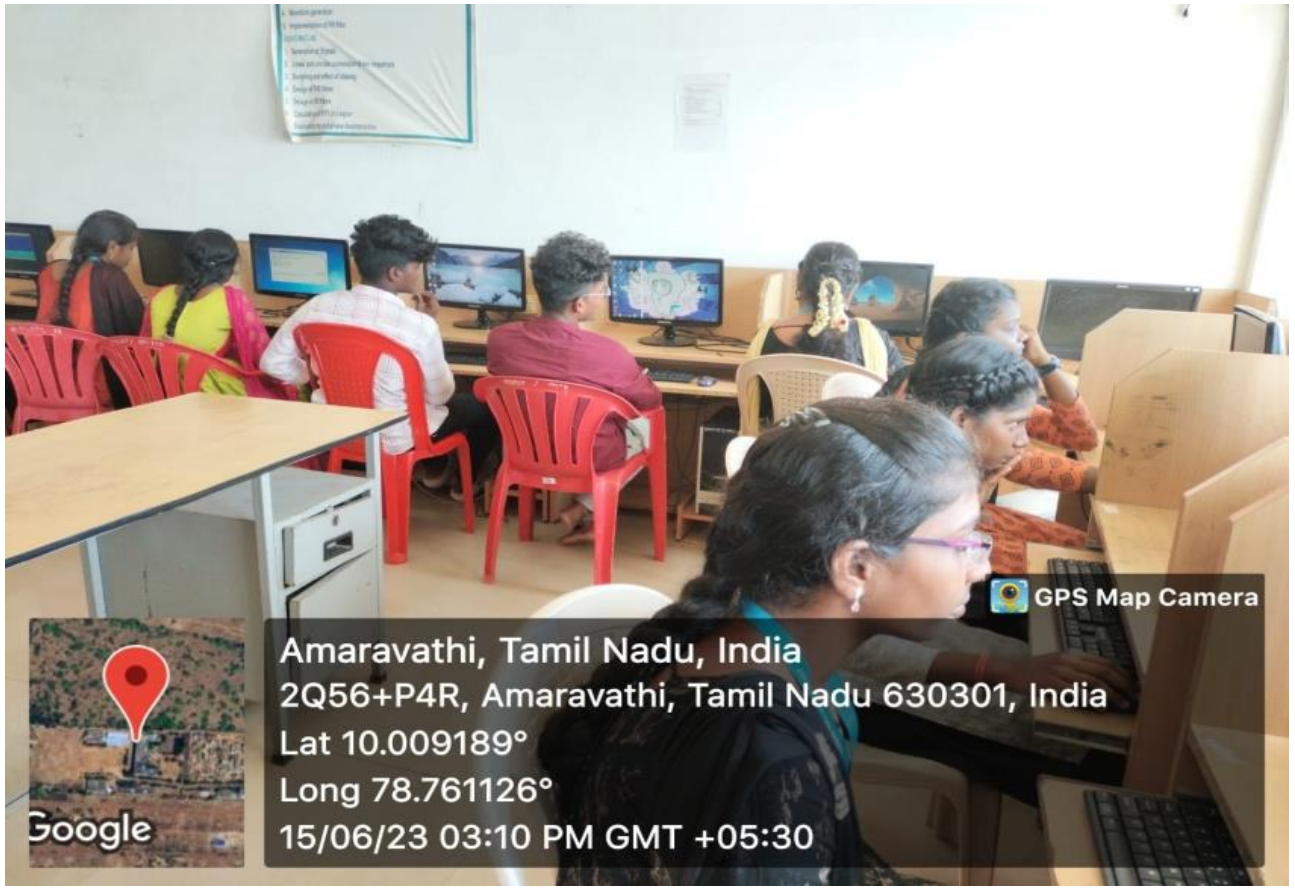
(ii). Generation of various signals and random noise



  
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# EMBEDDED LABORATORY



## (i).INTERFACING LED AND PWM

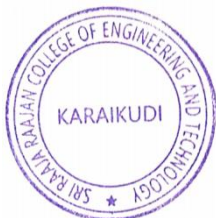


  
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## EMBEDDED LABORATORY



### **(ii).INTERFACING ADC AND DAC**




  
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## CIRCUIT DESIGN AND SIMULATION LABORATORY



### **(i).DESIGN AND ANALYSIS OF HARTLEY OSCILLATOR AND COLPITTS OSCILLATOR**



  
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# CIRCUIT DESIGN AND SIMULATION LABORATORY



## **(ii).DESIGN AND ANALYSIS OF SINGLE TUNED AMPLIFIER**



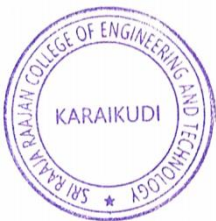
  
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## **ANALOG AND DIGITAL CIRCUITS LABORATORY**



### **(i). DESIGN OF REGULATOR POWER SUPPLIES**



  
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## **ANALOG AND DIGITAL CIRCUITS LABORATORY**



### **(ii). DESIGN OF FREQUENCY RESPONSE OF CE AMPLIFIER**



  
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## COMMUNICATION SYSTEM LABORATORY



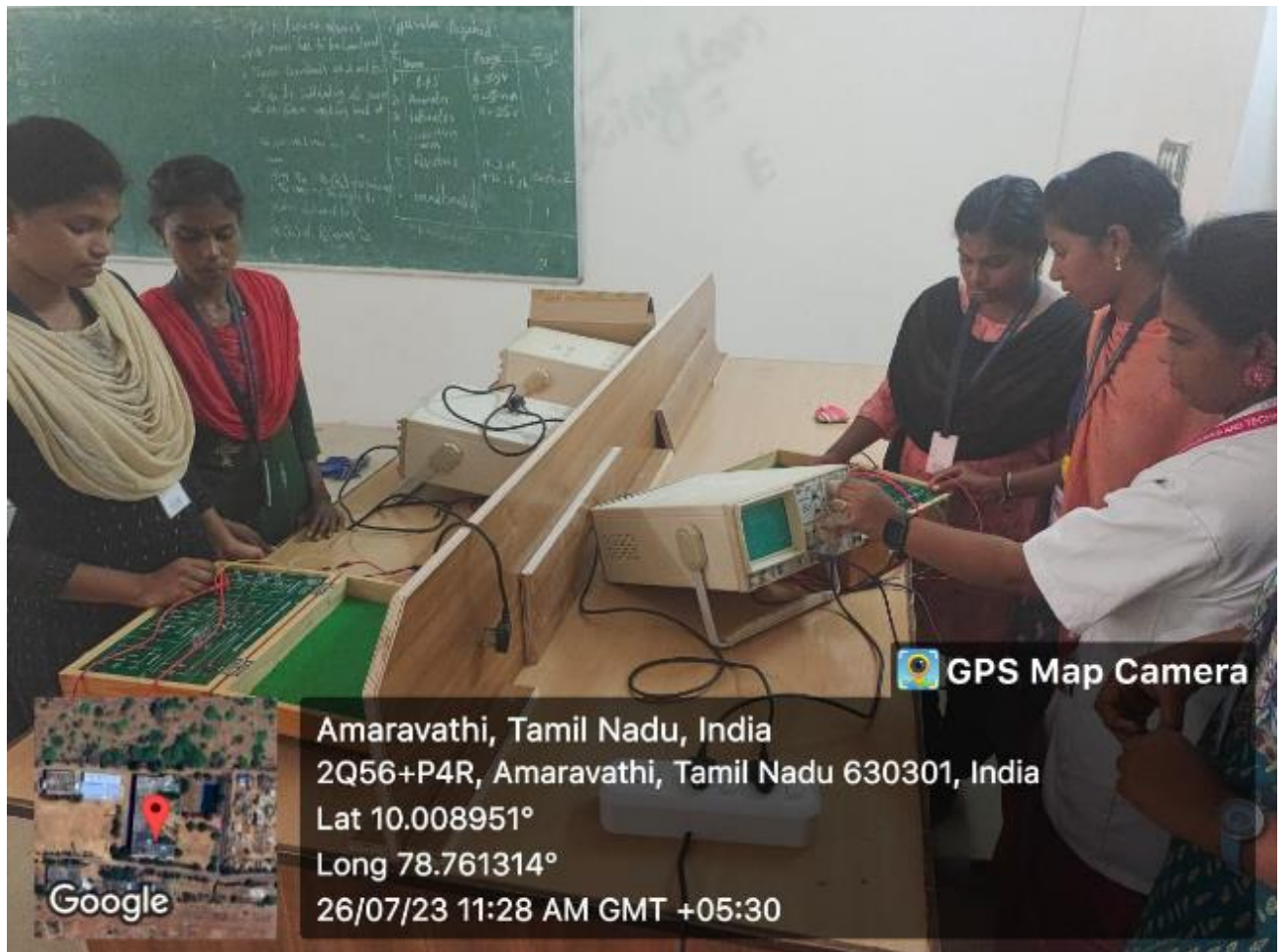
### **(i). DESIGN AND TESTING SIGNAL SAMPLING AND RECONSTRUCTION**



  
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## COMMUNICATION SYSTEM LABORATORY



### **(ii). DESIGN AND TESTING TIME DIVISION MULTIPLEXING**



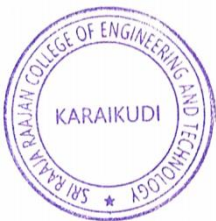
  
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# LINEAR INTEGRATED CIRCUITS LABORATORY



## (i). DESIGN AND TESTING INVERTING AMPLIFIER




  
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## LINEAR INTEGRATED CIRCUITS LABORATORY



### (ii). DESIGN AND TESTING NON INVERTING AMPLIFIER




  
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# VLSI LABORATORY



- (i). DESIGN AN ADDER (Min 8Bit) USING HDL. SIMULATE IT USING XILINX / ALTERA SOFTWARE AND IMPLEMENT BY XILINX / ALTERA FPGA.



  
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# VLSI LABORATORY



**(ii). DESIGN AN MULTIPLIER (Min 8Bit) USING HDL. SIMULATE IT USING XILINX / ALTERA SOFTWARE AND IMPLEMENT BY XILINX / ALTERA FPGA.**



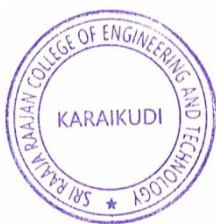
*[Signature]*  
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# MICROPROCESSORS AND MICROCONTROLLERS LABORATORY



(i). DESIGN OF BASIC ARITHMETIC AND LOGICAL OPERATIONS USING 8086 KIT

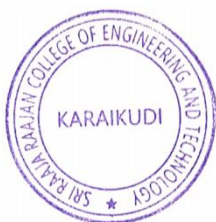


  
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# MICROPROCESSORS AND MICROCONTROLLERS LABORATORY



(ii). DESIGN OF MOVE A DATA BLOCK WITHOUT OVERLAP USING 8086 KIT



  
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